

FIG. 1
PRIOR ART

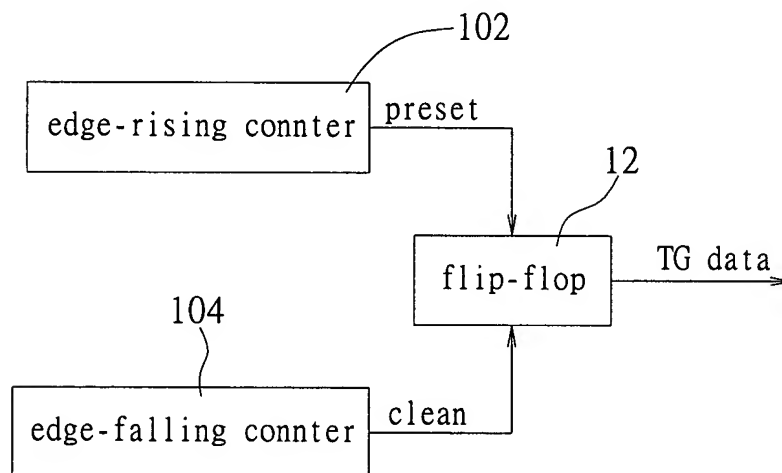


FIG. 2
PRIOR ART

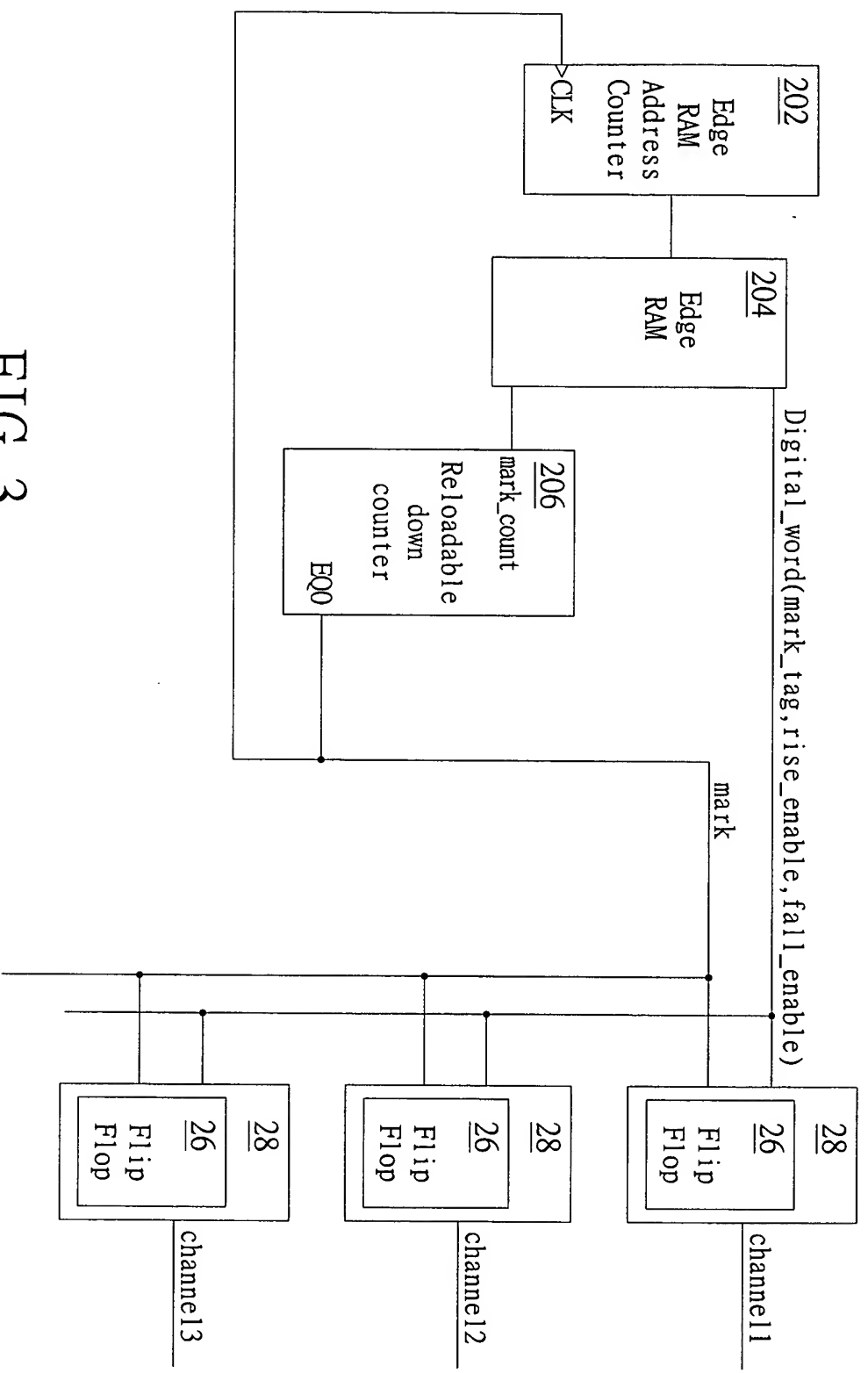


FIG. 3

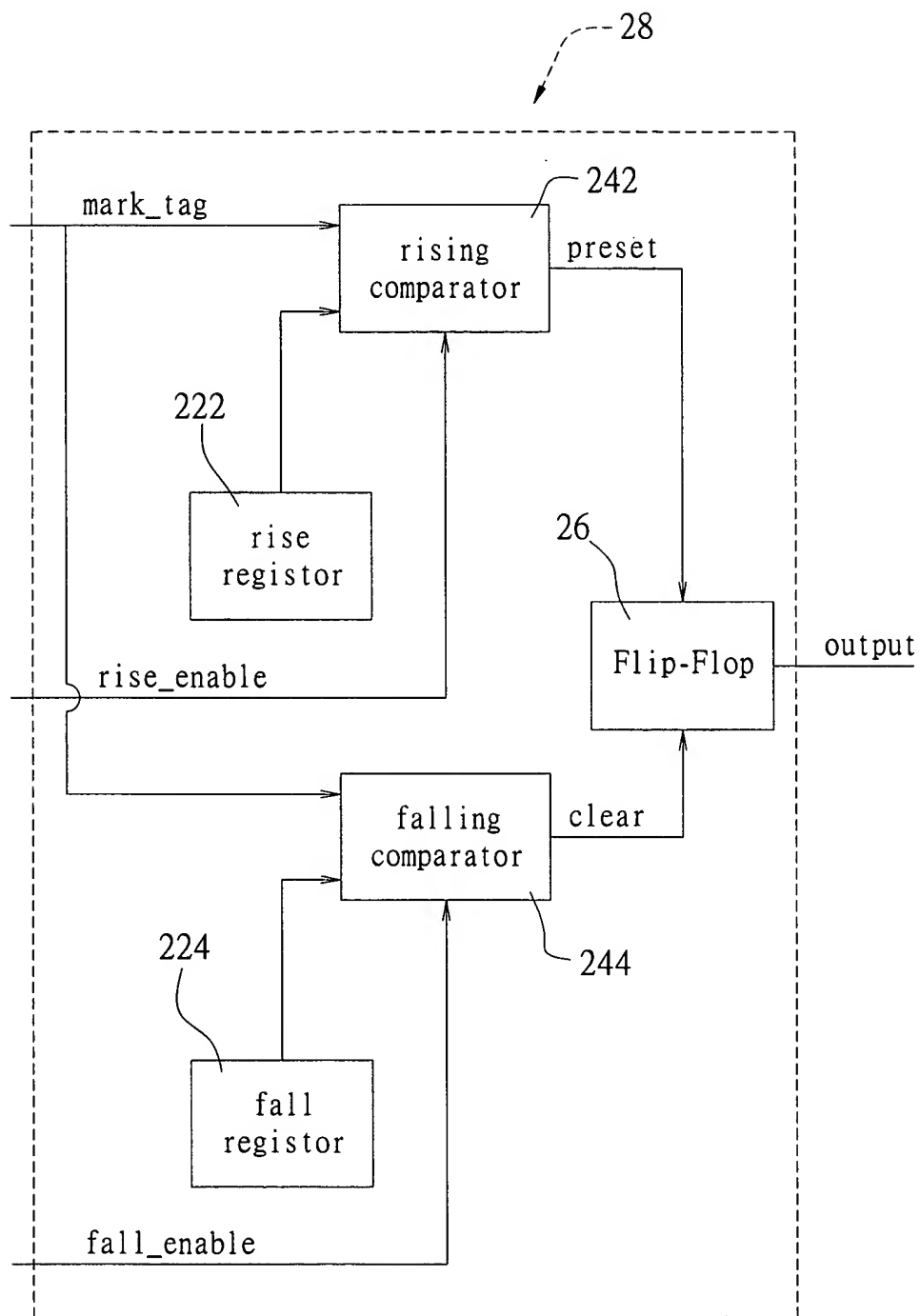


FIG. 4

ADDR	BIT 1-16 (MARK_COUNT)	BIT 17-20 (MARK_TAG)	BIT 21 (RISE_ENABLE)	BIT 22 (FALL_ENABLE)
0	2	1	1	0
1	2 (4-2)	2	1	0
2	2 (6-4)	3	0	1
3	2 (8-6)	1	1	0
4	2 (10-8)	4	1	1
5	2 (12-10)	3	0	1
6	3 (15-12)	5	0	1

FIG.5

RISE REGISTER OF CHANNEL1	1
FALL REGISTER OF CHANNEL1	3
RISE REGISTER OF CHANNEL2	2
FALL REGISTER OF CHANNEL2	4
RISE REGISTER OF CHANNEL3	4
FALL REGISTER OF CHANNEL3	5

FIG.6

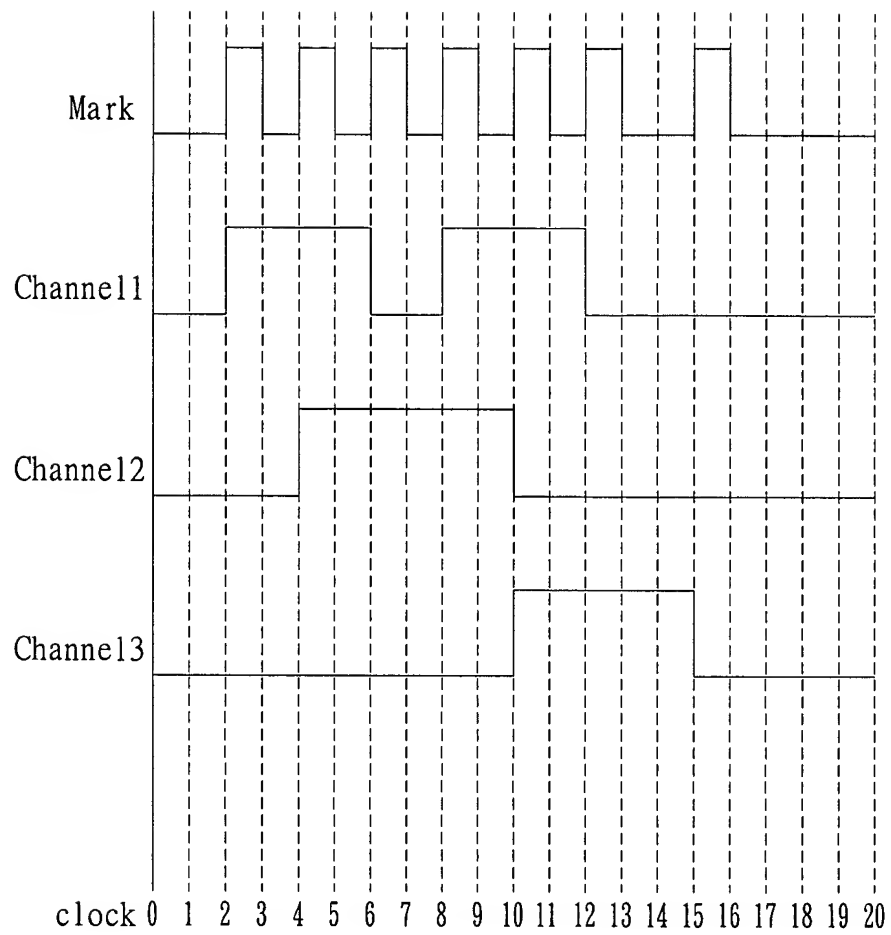


FIG. 7